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| Lawrence Bergman et al. | |

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U.S. PATENT DOCUMENT

EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

TP	1.	Hui-Ling Lou, IMPLEMENTING THE VITERBI ALGORITHM IEEE Signal Processing Magazine, pp. 42-52, September 1995
	2.	Yoshua Benjio, et al. INPUT - OUTPUT HMM'S FOR SEQUENCE PROCESSING IEEE Transactions on Neural Networks, Vol. 7., No. 5. pp. 1231-1249, September 1996
TP		

EXAMINER	/Thai Pham/	DATE CONSIDERED	06/27/2006

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